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A semiconductor device (100) is disclosed. In an

embodiment, a semiconductor device includes a N-well (114) within a P-well (112) in a silicon layer (107), positioned atop a buried oxide layer (109) of a silicon-on-insulator (SOI) substrate; a first source region (125) and a second source region (127) within a portion of the P-well (112); a first drain region (145) and a second drain region